

L Number	Hits	Search Text	DB	Time stamp
1	10984	"voltage divider" and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/07 15:25
2	2182	("voltage divider" and clock) and (driver or transmitter) and receiver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/07 15:25
3	18388	("710").CLAS.	USPAT; US-PGPUB	2004/04/07 15:25
5	18445	("713").CLAS.	USPAT; US-PGPUB	2004/04/07 15:26
6	41	((("voltage divider" and clock) and (driver or transmitter) and receiver) and ("713").CLAS.)	USPAT; US-PGPUB	2004/04/07 15:26
4	62	((("voltage divider" and clock) and (driver or transmitter) and receiver) and ("710").CLAS.)	USPAT; US-PGPUB	2004/04/07 16:30
7	1	("6456123").PN.	USPAT; US-PGPUB	2004/04/07 16:30
8	5	("4550424"   "4962325"   "5821780"   "5835045"   "5894284").PN.	USPAT	2004/04/07 16:31
9	0	6456123.URPN.	USPAT	2004/04/07 16:34
-	75606	"divider resistor" or shunt	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 14:34
-	8024	("divider resistor" or shunt) and clock	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 14:35
-	3447	((("divider resistor" or shunt) and clock) and driver	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:30
-	1151	((("divider resistor" or shunt) and clock) and driver) and receiver	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 14:36
-	1066	(710/100).CCLS.	USPAT; US-PGPUB	2004/04/06 14:36
-	4	((("divider resistor" or shunt) and clock) and driver) and receiver) and (710/100).CCLS.)	USPAT; US-PGPUB	2004/04/06 14:40
-	1058	((709/400) or (710/61) or (713/500)).CCLS.	USPAT; US-PGPUB	2004/04/06 14:41
-	8	((709/400) or (710/61) or (713/500)).CCLS.) and (((("divider resistor" or shunt) and clock) and driver) and receiver)	USPAT; US-PGPUB	2004/04/06 14:53
-	811	(713/400).CCLS.	USPAT; US-PGPUB	2004/04/06 14:53
-	1	((713/400).CCLS.) and (((("divider resistor" or shunt) and clock) and driver) and receiver)	USPAT; US-PGPUB	2004/04/06 14:54
-	1	((713/600).CCLS.) and (((("divider resistor" or shunt) and clock) and driver) and receiver)	USPAT; US-PGPUB	2004/04/06 14:54
-	613	(713/600).CCLS.	USPAT; US-PGPUB	2004/04/06 15:38

-	1939	synchron\$8 adj bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 15:39
-	374	(synchron\$8 adj bus) and (driver and receiver and clock)	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 16:16
-	1	("6646982").PN.	USPÄT; US-PGPUB	2004/04/06 16:16
-	0	6646982.URPN.	USPAT	2004/04/06 16:16
-	6	("5843799"   "6393504"   "6400771"   "6433600"   "6483755"   "6510503").PN.	USPAT	2004/04/06 16:16
-	1680	((("divider resistor" or shunt) and clock) and transmitter	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:32
-	1449	((("divider resistor" or shunt) and clock) and transmitter) and receiver	USPÄT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:31
-	18388	("710").CLAS.	USPÄT; US-PGPUB	2004/04/06 17:31
-	23	((("divider resistor" or shunt) and clock) and transmitter) and receiver) and ("710").CLAS.)	USPAT; US-PGPUB	2004/04/06 17:31
-	4270	((("divider resistor" or shunt) and clock) and (transmitter or driver)	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:45
-	1830	((("divider resistor" or shunt) and clock) and (transmitter or driver)) and receiver	USPÄT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:32
-	34	((("710").CLAS.) and (((("divider resistor" or shunt) and clock) and (transmitter or driver)) and receiver)	USPÄT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:46
-	18445	("713").CLAS.	USPÄT; US-PGPUB	2004/04/06 17:33
-	0	("18and16").PN.	USPAT; US-PGPUB	2004/04/06 17:33
-	43	((("713").CLAS.) and (((("divider resistor" or shunt) and clock) and (transmitter or driver)) and receiver)	USPAT; US-PGPUB	2004/04/06 17:47
-	4190	((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver)	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:46
-	73	((("713").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))	USPÄT; US-PGPUB	2004/04/06 17:47
-	68	((("710").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:50

-	4	((("710").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))) and ((("713").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))) not (117 or ((("713").CLAS.) and (((("divider resistor" or shunt) and clock) and (transmitter or driver)) and receiver))))	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:51
-	89	((("710").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))) or ((("713").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))) not (117 or ((("713").CLAS.) and (((("divider resistor" or shunt) and clock) and (transmitter or driver)) and receiver))))	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 18:03
-	32	((("713").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))) not ((("713").CLAS.) and (((("divider resistor" or shunt) and clock) and (transmitter or driver)) and receiver)))	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 17:52
-	35	((("710").CLAS.) and (((("center tapped" center-tapped or shunt) and clock) and (transmitter or driver))) not ((("710").CLAS.) and (((("divider resistor" or shunt) and clock) and (transmitter or driver)) and receiver)))	USPAT; US-PGPUB; JPO; DERWENT; IBM_TDB	2004/04/06 18:03